

**Technion Israel Institute of Technology  
VLSI Laboratory – Department of Electrical Engineering**

**Academic Use of Cadence Software Tools**

**Individual End Use Agreement (EUA) for University Account  
Students and Staff**

**Statement Acknowledging Responsibility To Use Cadence Tools  
in Accordance with End User Agreement**

EVERY university professor, student, or staff member who has access to proprietary software tools and documents from Cadence must sign the following agreement before they are granted access.

**STATEMENT ACKNOWLEDGING RESPONSIBILITY TO PROTECT PROPRIETARY MATERIAL**

My signature below acknowledges that:

1. I will use the Cadence tools only for my studies and research performed at the Technion and for no other purposed whatsoever.
2. I clearly understand that the use of the Cadence tools at the Technion are solely for non-commercial purposed, namely education and research.
3. I understand that proprietary material supplied through the VLSI lab at the Technion or any other means must be viewed only by myself and not shown to any other person. These documents may not be copied to or stored in any medium (softcopy or hardcopy) outside of the VLSI lab computers.
4. I am aware that use of all Cadence licenses are monitored and recorded by the VLSI lab as required by Cadence.

**Student 1 :**

Name: \_\_\_\_\_

Signature: \_\_\_\_\_

Email : \_\_\_\_\_

**Student 2 :**

(Printed) Name: \_\_\_\_\_

Signature: \_\_\_\_\_

Email : \_\_\_\_\_

User Account name in VLSI Lab. \_\_\_\_\_ ( ep \_\_\_\_ cp \_\_\_\_ mm \_\_\_\_ rf \_\_\_\_ mnr \_\_\_\_ ko \_\_\_\_ )

Date: \_\_\_\_\_

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